

# EC325 Microprocessors

## Bit Manipulations

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# REMINDER 1: General Info About String Instructions

- Source is always in DS:ESI
- Destination (if any) is always in ES:EDI
- To know the size of each element:
  1. Add two operands that are ignored but their size used (e.g. `movs ax,bx`)
  2. Add suffixes to instructor
    1. b (BYTE)
    2. w (WORD)
    3. d (DWORD)
- ESI/EDI are incremented/decremented after execution.
- Direction is controlled by DF (Direction Flag)
  - 1 means decrement (right to left)
  - 0 means increment (left to right)

# REMINDER 2: String Instructions

- MOVS[B|W|D]
  - Moves a string
- SCAS[B|W|D]
  - Scans a string
- STOS[B|W|D]
  - Stores a string
- LODS[B|W|D]
  - Loads a string
- CMPS[B|W|D]
  - Compare strings

# REMINDER 3: Repeating using REP

- REP INSTRUCTION
  - E.g. REP MOVS
- While  $CX > 0$ 
  - perform INSTRUCTION
  - $CX = CX - 1$
- END

# REMINDER 4: XLAT

- Uses a table to translate
- $n$  is converted to  $[EBX+n]$
- The input is put into AL before XLAT

```
table      BYTE 48 DUP (' '), '0123456789', 7 DUP (' ')
           BYTE 'abcdefghijklmnopqrstuvwxyz', 6 DUP (' ')
           BYTE 'abcdefghijklmnopqrstuvwxyz', 133 DUP (' ')

           mov   ecx, strLength ; string length
           lea  ebx, table      ; address of translation table
           lea  esi, string     ; address of string
           lea  edi, string     ; destination also string
forIndex:  lodsb                ; copy next character to AL
           xlat                ; translate character
           stosb               ; copy character back into string
           loop forIndex       ; repeat for all characters
```

# Logical Operation

- AND destination, source
- OR destination, source
- XOR destination, source
- NOT destination

# Examples

<i>Before</i>	<i>Instruction</i>	<i>Bitwise Operation</i>	<i>After</i>		
AX: E2 75 CX: A9 D7	and ax, cx	1110 0010 0111 0101 <u>1010 1001 1101 0111</u> 1010 0000 0101 0101	AX <table border="1"><tr><td>A0</td><td>55</td></tr></table> SF 1    ZF 0	A0	55
A0	55				
DX: E2 75 value: A9 D7	or dx, value	1110 0010 0111 0101 <u>1010 1001 1101 0111</u> 1110 1011 1111 0111	DX <table border="1"><tr><td>EB</td><td>F7</td></tr></table> SF 1    ZF 0	EB	F7
EB	F7				
BX: E2 75	xor bx, 0a9d7h	1110 0010 0111 0101 <u>1010 1001 1101 0111</u> 0100 1011 1010 0010	BX <table border="1"><tr><td>4B</td><td>A2</td></tr></table> SF 0    ZF 0	4B	A2
4B	A2				
AX: E2 75	not ax	<u>1110 0010 0111 0101</u> 0001 1101 1000 1010	AX <table border="1"><tr><td>1D</td><td>8A</td></tr></table>	1D	8A
1D	8A				

# Shift and Rotation Instructions

	Left	Right
Logical	SHL	SHR
Arithmetic	SAL	SAR

- $S^*$  destination, count
  - Count can be immediate or CL (mod 32)
- SHL, SAL are identical
- SAR sign extends the shifted bit
- SHR zero extends the shifted bit
- The bit that goes out is written to CF









# Rotation

- R[O|C][R|L] dest, count
  - Count can be immediate or CL (mod 32)
  - Like shift but the dropping bits are fed to the other side
  - RO\*
    - The last dropping bit is also copied to CF
  - RC\*
    - Assumes the CF is added as MSB to dest and does the rotation.

# Using Rotation

## Number to HEX

```
        lea    ebx,hexOut        ; address for first character
        mov    ecx,8            ; number of characters
forCount:  rol    eax,4            ; rotate first hex digit to end
        mov    edx,eax         ; copy all digits
        and    edx,0000000fh    ; zero all but last hex digit
        cmp    edx,9           ; digit?
        jnle   elseLetter      ; letter if not
        or     edx,30h         ; convert to character
        jmp    endifDigit
elseLetter: add    edx,'A' 10    ; convert to letter
endifDigit:
        mov    BYTE PTR [ebx],dl ; copy character to memory
        inc    ebx            ; point at next character
        loop  forCount        ; repeat
```